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Date: June 22, 2005

Debbie Casias
Debbie Casias

6/22/05
Date



Our Docket No: 042390P7143

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Jassowski et al.) Examiner: Zarneke, David A.
Application No: 09/475,643) Art Unit: 2891
Filed: December 30, 1999)
For: Optimized Driver Layout for)
Integrated Circuits with Staggered)
Bond Pads)

RESPONSE TO OFFICE ACTION

Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed March 22, 2005, Applicants respectfully request the Examiner to enter the following amendments and to consider the following remarks.

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